

V_{CE} =1200V I_{C} =150A

General Description

BYD IGBT Power Module BG150B12UY3-I provides fast switching characteristic as well as high short circuit capability, which introduce the advanced IGBT chip/FWD and improved connection.

Features

- High speed IGBT in trench/field stop technology
- Including ultra fast & soft recovery anti-parallel FWD
- Low inductance
- Standard package
- High short circuit capability
- Fast switching and short tail current

Applications

- High frequency drivers
- AC motor control
- Inverters
- Servo
- UPS (Uninterruptible Power Supplies)
- Electric welding



Characteristic Values

Parameter	Symbol	Conditions	Temperature	Value	Unit				
Absolute Maximum Ratings									
Collector-emitter voltage	V _{CES}	I _C =6mA,V _{GE} =0V	T _{vj} =25℃	1200	V				
Continuous collector current	Ic	_	T _c =80°C	150	А				
Peak collector current	I _{CRM}	I _{CRM} =2I _C	_	300	А				
Gate-emitter voltage	V _{GES}	_	_	+/-20	V				
Total power dissipation	P _{tot}	per switch (IGBT)	T _c = 25°C	960	W				
IGBT short circuit SOA	t _{psc}	V _{CC} =600V, V _{GE} ≤15V V _{CEM} ≤1200V	T _{vj} ≤125°C	10	us				
Max. junction temperature	T _{vj max}	_	_	150	$^{\circ}$ C				
Operation junction temperature	T _{vj op}	_	_	-40~125	$^{\circ}$				
Storage temperature range	T _{stg}	_	_	-40~125	$^{\circ}$ C				
Diode DC forward current	I _F	_	T _c =80 °C	150	А				
Peak forward current	I _{FRM}	I _{FRM} =2I _F	_	300	А				
l ² t-value, Diode	l ² t	V _R =0V,t=10ms	T _j =125℃	_	A ² s				
Isolation voltage	V _{isol}	t=1min,f=50Hz	_	2500	V				



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BG130B120								
Parameter	Symbol	Conditions	Temperature	Value			Unit	
		Characteristics					1	
IGBT				min.	typ.	max.		
Gate-emitter threshold voltage	V _{GE(th)}	I _C =6mA ,V _{GE} =V _{CE}	T _{vi} =25℃	5.0	6.0	6.5	V	
Collector-emitter cut-off current		V _{CE} =1200V,V _{GE} =0V	T _{vi} =25°C	_	_	1.0	mA	
	I _{CES}		T _{vi} =125℃	_	_	10	mA	
Gate-emitter cut-off current	I _{GES}	$V_{CE}=0V, V_{GE}=\pm 20V$	T _{vi} =25℃	-300	_	300	nA	
Collector-emitter		Ic=150A,V _{GE} =15V	T _{vi} =25℃	_	2.5	_	V	
saturation voltage	V _{CE(sat)}		T _{vi} =125℃	_	3.2	_	V	
Integrated gate resistor	R _{Gint}	_	T _{vi} =25℃	_	_	_	Ω	
Total Gate Charge	Qg	V _{CE} =600V,Ic=150A, V _{GE} =±15V	<u> </u>		0.92		uC	
Gate-Emitter Charge	Q _{ge}		_		0.16		uC	
Gate-Collector Charge	Q _{gc}		_		0.31		uC	
Input capacitance	Cies	V _{CE} =25V,V _{GE} =0V, f=1MHz	T _{vj} =25℃	_	tbd	_	nF	
Output capacitance	Coes			_	tbd	_	nF	
Reverse transfer capacitance	Cres			_	tbd	_	nF	
·		V_{CC} =600V,Ic=150A, R_{Gon} = R_{Goff} =3.3 Ω , V_{GE} =±15V, L_{σ} =80nH, Inductive load	T _{vi} =25℃	_	468	_	ns	
Turn-on delay time	$t_{d(on)}$		T _{vi} =125℃	_	479	_	ns	
Rise time	t _r			_	101	_	ns	
			T _{vi} =125℃	_	77	_	ns	
Turn-off delay time			T _{vi} =25℃	_	541	_	ns	
	$t_{d(off)}$		T _{vi} =125℃	_	579	_	ns	
Fall time	t _f		T _{vi} =25℃	_	135	_	ns	
			T _{vj} =125℃	_	150	_	ns	
Energy dissipation during turn-on time	E _{on}	$V_{\text{CC}}\text{=}600\text{V},\text{lc}\text{=}150\text{A},$ $R_{\text{Gon}}\text{=}3.3\Omega,\text{V}_{\text{GE}}\text{=}\pm15\text{V}$ $L_{\sigma}\text{=}80\text{nH},$ Inductive load	T _{vj} =25°C	_	10.9	_	mJ	
			T _{vj} =125℃	_	13.7	_	mJ	
Energy dissipation during turn-off time	E _{off}	$ \begin{array}{c c} V_{\text{CC}}\text{=}600\text{V,Ic}\text{=}150\text{A}, \\ R_{\text{Goff}}\text{=}3.3\Omega, V_{\text{GE}}\text{=}\pm15\text{V} \\ L_{\sigma}\text{=}80\text{nH}, \\ \text{Inductive load} \\ \end{array} \begin{array}{c} T_{vj}\text{=}25^{\circ}\text{C} \\ \\ T_{vj}\text{=}125^{\circ}\text{C} \end{array} $	T _{vj} =25℃	_	8.0	_	mJ	
			_	11	_	mJ		
Diode				min.	typ.	max.		
	V _F	I _F =150A	T _{vj} =25°C	_	1.7	_	V	
Forward voltage			T _{vj} =125℃	_	1.7	_	V	
Peak reverse recovery current	I _{RR}			_	134	_	А	
Recovered charge	Qrr	I _F =150A,V _R =600V, di _F /dt=1300A/us	T _{vj} =125℃	_	20.5	_	uC	
Reverse recovery time	t _{rr}		T _{vj} =125℃	_	356	_	ns	
Reverse recovery energy	E _{rec}		T _{vi} =125℃	_	8.8		mJ	



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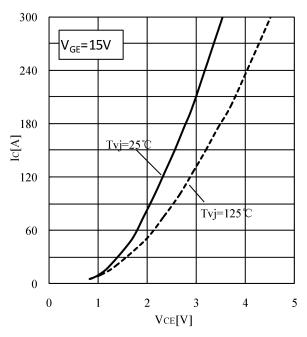
Parameter	Symbol	Conditions		min.	typ.	Max.	Unit		
Thermal-Mechanical Specifications									
IGBT thermal resistance junction to case	R _{th(j-c)}	per IGBT		_	0.13	_	K/W		
Diode thermal resistance junction to case	R _{th(j-c)}	per diode		_	0.20	_	K/W		
Thermal resistance case to heat-sink	R _{th(c-s)}	per module		_	0.03	_	K/W		
Dimensions	LxWxH	Typical , see outline drawing		106.4 x 61.4 x 31.5		mm			
Clearance distance in air	da	according to IEC 60664-1 and EN 50124-1	Term. to base:	_	_	28.3			
			Term. to term:	6.0	_	_	mm		
Surface creepage distance	ds	according to IEC 60664-1and EN 50124-1	Term. to base:	-	24	_			
			Term. to term:		14	_	mm		
Mass	m	_	_	_	320	_	g		

Thermal and mechanical properties according to IEC 60747 – 15

Specification according to the valid application note.



Characterization Curves



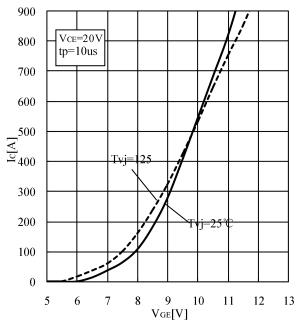
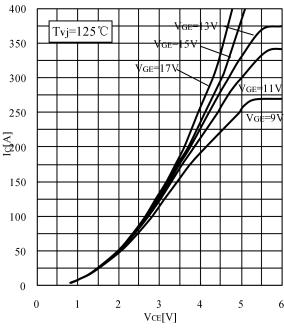
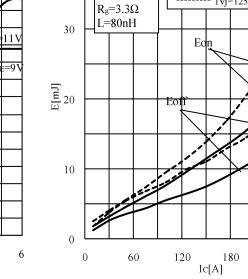


Fig.1 Typ. On-state Characteristics

Fig.2 Typ. Transfer Characteristics

Tvj=125℃





Rev.A/0

Vcc=600V

V_{GE}=15V

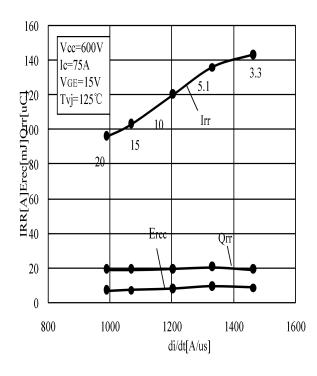
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Fig.3 Typ. Output Characteristics

Fig.4 Switching Loss vs. Collector Current

300

240



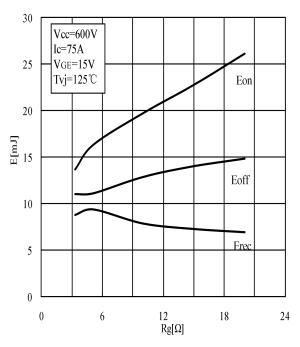


Fig.5 Typ. Reverse Recovery Characteristics vs di/dt

Fig.6 Switching Loss vs. Gate Resistor

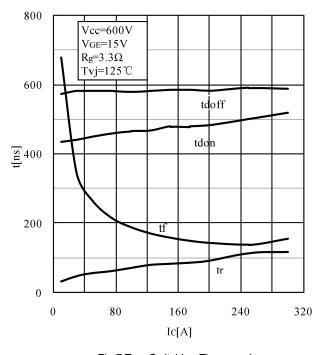


Fig.7 Typ. Switching Times vs. $\ensuremath{I_{\text{C}}}$

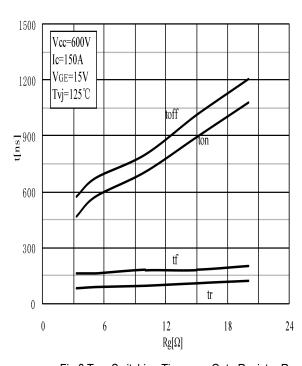
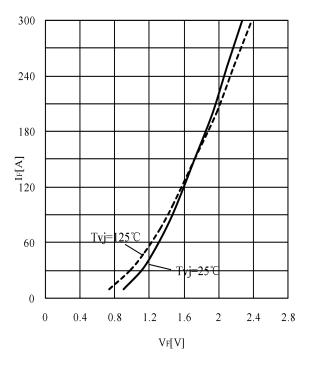


Fig.8 Typ. Switching Times vs. Gate Resistor $R_{\text{\scriptsize G}}$

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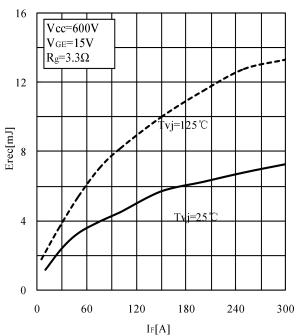
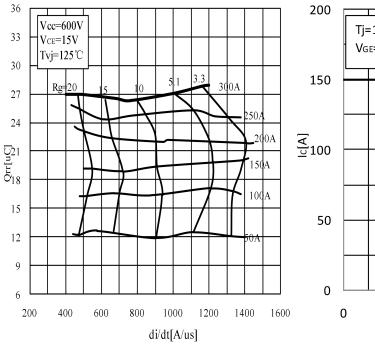


Fig.9 FWD Forward Characteristics.

Fig.10 Typ. Switching Losses Diode-Inverter





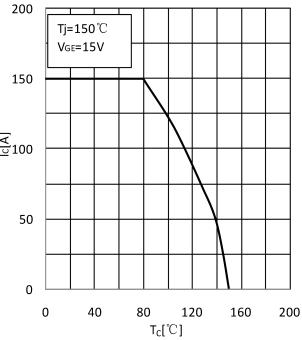
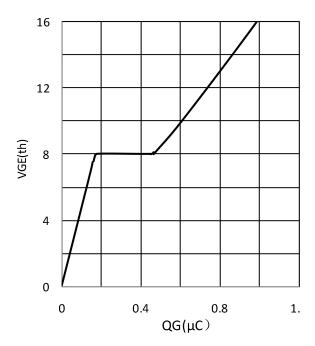


Fig. 12 Rate Current vs. Temperature (T_C)



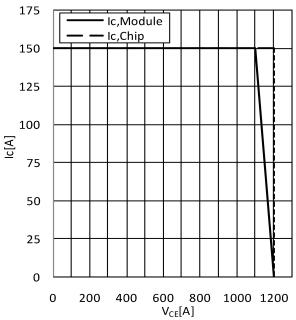


Fig.13 Typ. Gate Charge Characteristics

Fig.14 Reverse Bias Safe Operating Area IGBT-inv(RBSOA)

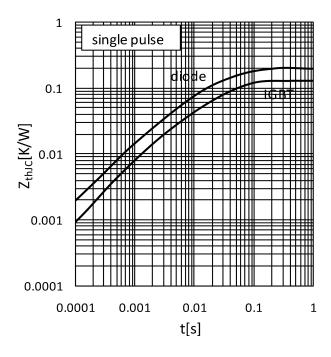
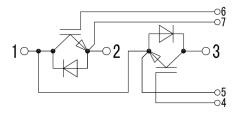


Fig.15 Typ. Transient Thermal Impedance

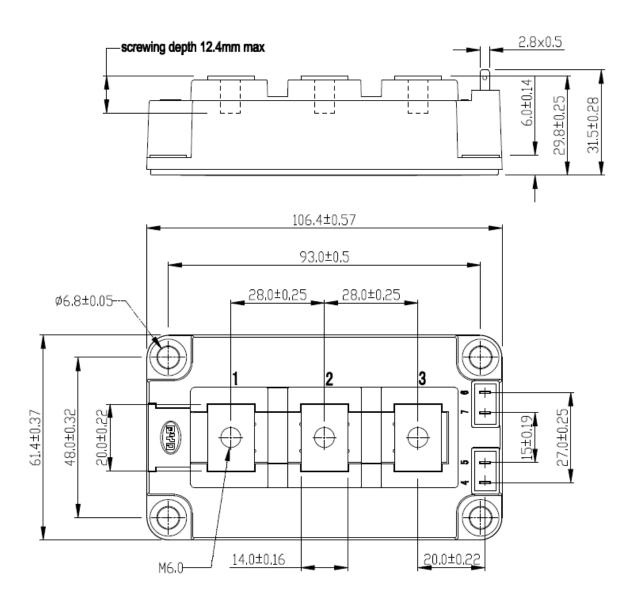


Circuit Diagram



Package Outlines

Dimensions in mm



Attached (recommended torque):



 M_S : (to heat sink M6) 3~6 Nm M_t : (to terminals M6) 2.5~5Nm

Attention

- 1. In order to reduce the contact resistance, we suggest add thermal grease between base and heat-sink, which thickness is about 0.1mm.
- 2. When installing the module, please wear a electrostatic bracelet to prevent the gate breakdown and the imbalance power may damage the internal chip, even to damage the module.
- 3. This is an electrostatic sensitive device, please observe the international standard IEC 60747-1, chap. IX.

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